

In the Claims

Please amend the claims as follows:

1        1. (Previously Amended) A method of in circuit emulation of  
2 an integrated circuit including a digital data processor capable of  
3 executing program instructions, the integrated circuit including a  
4 serial scan path for control of the state of the integrated  
5 circuit, comprising the steps of:

6        detecting a predetermined debug event;  
7        upon detection of said predetermined debug event suspending  
8 program execution except for at least one type interrupt;  
9        executing an emulation monitor program via said at least one  
10 type interrupt; and  
11        selectively assigning control of at least one emulation  
12 resource of the integrated circuit to one of said serial scan path  
13 or said monitor program.

2. (Canceled)

1        3. (Previously Amended) The method of claim 1, wherein the  
2 integrated circuit includes a monitor privilege input, said method  
3 wherein:

4        said step of selectively assigning emulation resources of the  
5 integrated circuit assigns said emulation resources to said serial  
6 scan path upon a first digital state of said monitor privilege  
7 input and assigns said emulation resources to said emulation  
8 monitor program upon a second digital state of said monitor  
9 privilege input.

1        4. (Previously Amended) The method of claim 1, the emulation  
2 resources include at least one read write data register, said  
3 method further comprising:

4        said step of selectively assigning emulation resources of the  
5 integrated circuit includes accessing said at least one read write  
6 data register.